

AMENDMENTS TO THE CLAIMS

The claims in this listing replaces all prior versions and listings of claims in the application. Applicants note that these claims have not been revised from the prior submission, but are presented here for the convenience of the Examiner.

Listing of Claims:

1. (Previously Presented) A digital recording and reproducing apparatus which is capable of having a removable external memory mounted therein, for performing recording of recording data in the external memory and reproduction based on the recording data recorded in the external memory,

the digital recording and reproducing apparatus comprising:

an identification data-generating block that generates identification data that identifies the external memory individually;

an internal memory in which the identification data can be recorded; and

a control block that records the identification data in the external memory and said internal memory, and carries out identification data determination processing that determines, when mounting of the external memory is detected, whether or not the identification data recorded in the external memory and the identification data recorded in said internal memory are identical to each other, and to display a message that notifies that a different external memory has been mounted when the identification data are different from each other.

2. (Original) A digital recording and reproducing apparatus according to claim 1, wherein said control block records the identification data in both of the external memory and said internal memory before the recording data is recorded in the external memory.

3. (Previously Presented) A digital recording and reproducing apparatus according to claim 2, wherein said control block carries out the identification data determination processing when the external memory is mounted after having been removed during recording of the recording data in the external memory, said control block continuing to record the recording data in the mounted external memory when the identification data recorded in the external memory and the identification data recorded in said internal memory are identical to each other, and carrying out predetermined error-handling processing when the identification data recorded in the external memory and the identification data recorded in said internal memory are different from each other.

4. (Previously Presented) A digital recording and reproducing apparatus according to claim 2, wherein said control block causes the recording data to be recorded in said internal memory when the external memory is removed during recording of the recording data in the external memory, carries out the identification data determination processing when the external memory is mounted, and records the recording data recorded in said internal memory in the mounted external memory when the identification data recorded in the external memory and the identification data recorded in said internal memory are identical to each other.

5. (Previously Presented) A digital recording and reproducing apparatus according to claim 1, wherein said identification data-generating block generates numerical value information at random to thereby generate the identification data.

6. (Previously Presented) A digital recording and reproducing apparatus according to claim 1, wherein the recording data is sound data, and wherein the digital recording and reproducing apparatus includes a sound signal input block for inputting an analog sound signal, a sound data generation block for generating the sound data by converting the analog sound signal to digital data and compressing the digital data, a sound signal generation block for decompressing the sound data recorded in the external memory to generate the digital data and converting the digital data to the analog sound signal, and an amplifier circuit for amplifying the converted analog sound signal to output the amplified sound signal.

7. (Original) A digital recording and reproducing apparatus according to claim 5, wherein the recording data is sound data, and wherein the digital recording and reproducing apparatus includes a sound signal input block for inputting an analog sound signal, a sound data generation block for generating the sound data by converting the analog sound signal to digital data and compressing the digital data, a sound signal generation block for decompressing the sound data recorded in the external memory to generate the digital data and converting the digital data to the analog sound signal, and an amplifier circuit for amplifying the converted analog sound signal to output the amplified sound signal.

8. (Previously Presented) A digital recording and reproducing apparatus according to claim 2, wherein said identification data-generating block generates numerical value information at random to thereby generate the identification data.

9. (Previously Presented) A digital recording and reproducing apparatus according to claim 3, wherein said identification data-generating block generates numerical value information at random to thereby generate the identification data.

10. (Previously Presented) A digital recording and reproducing apparatus according to claim 4, wherein said identification data-generating block generates numerical value information at random to thereby generate the identification data.

11. (Previously Presented) A digital recording and reproducing apparatus according to claim 2, wherein the recording data is sound data, and wherein the digital recording and reproducing apparatus includes a sound signal input block for inputting an analog sound signal, a sound data generation block for generating the sound data by converting the analog sound signal to digital data and compressing the digital data, a sound signal generation block for decompressing the sound data recorded in the external memory to generate the digital data and converting the digital data to the analog sound signal, and an amplifier circuit for amplifying the converted analog sound signal to output the amplified sound signal.

12. (Previously Presented) A digital recording and reproducing apparatus according to claim 3, wherein the recording data is sound data, and wherein the digital recording and reproducing apparatus includes a sound signal input block for inputting an analog sound signal, a sound data generation block for generating the sound data by converting the analog sound signal to digital data and compressing the digital data, a

sound signal generation block for decompressing the sound data recorded in the external memory to generate the digital data and converting the digital data to the analog sound signal, and an amplifier circuit for amplifying the converted analog sound signal to output the amplified sound signal.

13. (Previously Presented) A digital recording and reproducing apparatus according to claim 4, wherein the recording data is sound data, and wherein the digital recording and reproducing apparatus includes a sound signal input block for inputting an analog sound signal, a sound data generation block for generating the sound data by converting the analog sound signal to digital data and compressing the digital data, a sound signal generation block for decompressing the sound data recorded in the external memory to generate the digital data and converting the digital data to the analog sound signal, and an amplifier circuit for amplifying the converted analog sound signal to output the amplified sound signal.

14. (Previously Presented) A digital recording and reproducing apparatus according to claim 8, wherein the recording data is sound data, and wherein the digital recording and reproducing apparatus includes a sound signal input block for inputting an analog sound signal, a sound data generation block for generating the sound data by converting the analog sound signal to digital data and compressing the digital data, a sound signal generation block for decompressing the sound data recorded in the external memory to generate the digital data and converting the digital data to the analog sound signal, and an amplifier circuit for amplifying the converted analog sound signal to output the amplified sound signal.

15. (Previously Presented) A digital recording and reproducing apparatus according to claim 9, wherein the recording data is sound data, and wherein the digital recording and reproducing apparatus includes a sound signal input block for inputting an analog sound signal, a sound data generation block for generating the sound data by converting the analog sound signal to digital data and compressing the digital data, a sound signal generation block for decompressing the sound data recorded in the external memory to generate the digital data and converting the digital data to the analog sound signal, and an amplifier circuit for amplifying the converted analog sound signal to output the amplified sound signal.

16. (Previously Presented) A digital recording and reproducing apparatus according to claim 10, wherein the recording data is sound data, and wherein the digital recording and reproducing apparatus includes a sound signal input block for inputting an analog sound signal, a sound data generation block for generating the sound data by converting the analog sound signal to digital data and compressing the digital data, a sound signal generation block for decompressing the sound data recorded in the external memory to generate the digital data and converting the digital data to the analog sound signal, and an amplifier circuit for amplifying the converted analog sound signal to output the amplified sound signal.